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Interleaver for mapping symbols on the carriers of an OFDM system ...

The interleaver memory reads-out the data symbols on to the OFDM carriers to effect the mapping, the read-out being in a different order than the read-in, ...

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Interleaver for mapping symbols on the carriers of an OFDM system ...

The interleaver memory reads-out the data symbols on to the OFDM carriers to effect ...

The set of addresses are generated from an address generator which ...

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EP673131 Tokyo european software patent - Hierarchical ofdm system ...

In this interleaver the interleaving is performed in conformity with the rule ... Phases and amplitudes of the respective OFDM carriers (or I signal and Q ...

gauss.ffil.org/PatentView/EP673131 - 76k - [Cached](#) - [Similar pages](#) - [Note this](#)

OFDM communication channel - US Patent 6985432

Some of the channels may include a small convolutional interleaver 847. The transport stream is, therefore, OFDM Carriers=2048 Carriers in use=1792 ...

www.patentstorm.us/patents/6985432-description.html - 85k -

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Decoders for many-carrier signals, in particular in DVB-T ...

These comprise an outer convolutional interleaver and an inner interleaver. and out-of-phase) carrier components for successive OFDM carriers. ...

www.patentstorm.us/patents/7221720-description.html - 79k -

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(WO/2001/056239) CLOCK SYNCHRONIZATION, FREQUENCY SYNCHRONIZATION ...

Some of the channels may include a small convolutional interleaver 847. Bandwidth =

28 MHz OFDM Carriers = 2048 Carriers in use = 1792 Sample Rate = 28 ...

www.wipo.int/pctdb/en/wo.jsp?WO=2001%2F056239&IA=WO2001%2F056239&DISPLAY=DESC - 110k - [Cached](#) - [Similar pages](#) - [Note this](#)

(WO/2002/009298) WIRELESS INTERACTIVE SYSTEM AND METHOD

Similarly, a small convolutional interleaver may be used. ... 12 details the downstream OFDM carriers allocation in the frequency domain, with frequency ...

www.wipo.int/pctdb/en/wo.jsp?IA=WO2002009298&DISPLAY=DESC - 141k -

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Ofdm communication channel invention

... an address generator for generating a write address signal and a read a convolutional interleaver 832, convolutional encoding and puncturing unit ...

freshpatents.com/Ofdm-communication-channel-dt20050922ptan20050207334.php?type=description - 91k - [Cached](#) - [Similar pages](#) - [Note this](#)

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<http://www.google.com/search?hl=en&rls=GGLD%2CGGLD%3A2004-30%2CGGLD%3A...> 2/28/08

Time and frequency domain interleaving and convolution code are used to In order to generate the 1536 modulated OFDM carriers in DAB mode I, an IFFT of ...
etd.ncku.lib.ncku.edu.tw/ETD-db/ETD-search/getfile?URN=etd-0705102-031619&filename=etd-0705102-031619.pdf - Similar pages - Note this

An OFDM receiver obtains, for each symbol, a composite channel ...
36 plurality of symbols, and means (404-408) for interleaving the contents of the and out-of-phase) carrier components for successive OFDM carriers. ...
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Interleaver for mapping symbols on the carriers of an OFDM system ...

The address provides an indication of one of the OFDM carriers for carrying ... The

address generator comprises a linear feedback shift register including ...

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Interleaver for mapping symbols on the carriers of an OFDM system ...

The data processor includes an interleaver memory which reads-in the ... from an address generator which comprises a linear feedback shift register and a ...

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Data processing apparatus and method - IP.com's Patent Debate

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Inventor:	PERON JEAN-LUC (GB)	Applicant: SONY UK LTD (GB)
EC:	H04L27/26M5	IPC: H03M13/27; H04L27/26; H04N5/44 (+8)
Publication Info. EP1463255 - 2004-09-29		

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Inventor:	PERON JEAN-LUC (FR)	Applicant: SONY UK LTD (GB)
EC:	H03M13/27; H04L27/26M5; (+1)	IPC: H04L27/26; H03M13/27; H04N5/44 (+5)
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 So, D.K.C.; Cheng, R.S.;
 Wireless Communications, IEEE Transactions on
 Volume 5, Issue 6, June 2006 Page(s):1217 - 1222
 Digital Object Identifier 10.1109/TWC.2006.1638637
[AbstractPlus](#) | Full Text: PDF(216 KB) IEEE JNL
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2. Group OFDM technique for beyond 3G
 Author, F.A.; Author, S.B., Jr.; Author, T.C.;
 Personal, Indoor and Mobile Radio Communications, 2005. PIMRC 2005. IEE
 Symposium on
 Volume 2, 11-14 Sept. 2005 Page(s):963 - 966 Vol. 2
 Digital Object Identifier 10.1109/PIMRC.2005.1651584
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3. Optimized software implementation of a full-rate IEEE 802.11a compliant transmitter on a digital signal processor
 Yiyan Tang; Lie Qian; Yuke Wang;
 Global Telecommunications Conference, 2005. GLOBECOM '05. IEEE
 Volume 4, 28 Nov.-2 Dec. 2005 Page(s):5 pp.
 Digital Object Identifier 10.1109/GLOCOM.2005.1578053
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4. Cyclic delay diversity with bit-interleaved coded modulation in orthogonal multiple access
 Bauch, G.; Malik, J.S.;
 Wireless Communications, IEEE Transactions on
 Volume 5, Issue 8, Aug. 2006 Page(s):2092 - 2100
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- ☐ 1. [Interleaver for mapping symbols on the carriers of an OFDM system](#)
 Peron, Jean-Luc (SONY UNITED KINGDOM LIMITED), *EUROPEAN PATENT APPLICATION*, May 2006
 patno:EP1662740
 ...one of the OFDM carriers for carrying...stored in the interleaver memory,
 in...comprises an interleaver memory operable...symbols for the OFDM carriers to
 effect...determined by an address generator, an address...comprises a linear
 feedback shift register including...
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- ☐ 2. [Interleaver for mapping symbols on the carriers of an OFDM system](#)
 Peron, Jean-Luc (SONY UNITED KINGDOM LIMITED), *EUROPEAN PATENT APPLICATION*, May 2006
 patno:EP1662739
 ...one of the OFDM carriers for carrying...stored in the interleaver memory,
 in...comprises an interleaver memory operable...symbols for the OFDM carriers to
 effect...determined by an address generator, an address...comprises a linear
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- ☐ 3. [Interleaver for mapping symbols on the carriers of an OFDM system](#)
 Peron, Jean-Luc (SONY UNITED KINGDOM LIMITED), *EUROPEAN PATENT APPLICATION*, Sep 2004
 patno:EP1463255
 ...one of the OFDM carriers for carrying...stored in the interleaver memory,
 in...comprises an interleaver memory operable...symbols for the OFDM carriers to
 effect...determined by an address generator, an address...comprises a linear
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- ☐ 4. [Interleaver for mapping symbols on the carriers of an OFDM system](#)
 Peron, Jean-Luc (SONY UNITED KINGDOM LIMITED), *EUROPEAN PATENT*, Sep 2004
 patno:EP1463256
 ...one of the OFDM carriers for carrying...stored in the interleaver memory,

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in...comprises an interleaver memory operable...symbols for the OFDM carriers to effect...determined by an address generator, an address...comprises a linear feedback shift register including...

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-  5. Data processing apparatus and method
Peron, Jean-Luc, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Dec 2004
patno:US20040246888
...0044] An address generator 102.2 for...parts of the address generator for the 8...using a LFSR (Linear Feedback Shift Register). [0046] The...340 and an address generator 342. The interleaver memory is...
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Application Number Information

Application Number: 10/806524

Assignments

Examiner Number: 80488 / TORRES, JUAN

Filing or 371(c) Date: 03/23/2004 eDan

Group Art Unit: 2611

IFW Madras

Effective Date: 03/23/2004

Class/Subclass:

375/260.000

Application Received: 03/24/2004

Lost Case: NO

Waiting for Response

Pat. Num./Pub. Num: /20040246888

Interference Number:

Desc,

Issue Date: 00/00/0000

Unmatched Petition: NO

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Date of Abandonment: 00/00/0000

L&R Code; Secrecy Code:1

Attorney Docket Number: 282575US8X

Third Level Review: NO

Secrecy Order: NO

Status: 51 /EX PARTE QUAYLE ACTION MAILED

Status Date: 12/10/2007

Confirmation Number: 2739

Oral Hearing: NO

Title of Invention: DATA PROCESSING APPARATUS AND METHOD OPERABLE TO MAP
AND DE-MAP SYMBOLS AND CARRIER SIGNALS OF AN ORTHOGONAL FREQUENCY
DIVISION MULTIPLEXED (OFDM) SYMBOL

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WIDS	2004-03-23	14	Y <input checked="" type="checkbox"/>	2007-11-06 15:57:41.0	jtorres1
<input type="button" value="Update"/>					

Inventor Information for 10/806524

Inventor Name	City	State/Country
PERON, JEAN-LUC	SURESNES	FRANCE

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
10806524	Not Issued	51	03/23/2004	Data processing apparatus and method operable to map and de- map symbols and carrier signals of an Orthogonal Frequency Division Multiplexed (OFDM) symbol	PERON, JEAN-LUC

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